

A26
Cont.

the differential voltage ΔV_{BE} of the base-to-emitter voltages (V_{BE1} and V_{BE2} , respectively) of said first and second bipolar transistors is of a value proportional to V_T (thermal voltage) having a positive temperature characteristic;

the reference voltage output from said number ($K2 + 1$) differential pair being given by $V_{BE2} + K2 \times \Delta V_{BE}$.

IN THE DRAWINGS:

Please amend FIGS. 1 and 5-9 of the drawings as shown in red in the attached copies of the drawings.

REMARKS

In response to the election requirement, Applicant confirms the election of Group I, claims 1-11, 15-21, 26 and claim 27, which was inadvertently misnumbered originally as claim 20. It is respectfully requested that the non-elected claims be maintained in this Application, without further action thereon, for possible rejoinder and/or for filing one or more divisional applications.

The indicated allowability of claims 2-11, 17-21 and 27 is noted, with thanks. These claims have all been amended to overcome the pending objections and rejections and are believed to be in condition for allowance. These amendments are discussed in greater detail below.

Claims 2, 3 and 4 have been rewritten to include the limitations of former claim 1, from which they depended, and these claims also now specify that the current mirror circuit has an input end connected to the output terminal of the first OTA and an output end connected to the

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